

04-28-05

IFW



Docket No. 60549 (70801)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application: Nakai, et al. Examiner: Nhu, David
Application No. 10/755,019 Art Unit: 2818
Filed: January 8, 2004 Confirmation: 8951
For: Process for manufacturing semiconductor device and semiconductor device

**Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

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CERTIFICATE OF EXPRESS MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service with sufficient postage as Express Mail Label No. **EV 492343932 US** addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on April 26, 2005.

By: *Helen Murray Tarbi*
Helen Murray Tarbi

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RESPONSE TO RESTRICTION REQUIREMENT MAILED APRIL 1, 2005

In the Office Action dated April 1, 2005, restriction is required to one of the following inventions:

- I Claims 1-5, drawn to a method/process for manufacturing a semiconductor device;
- II Claims 6-9, drawn to a semiconductor device.

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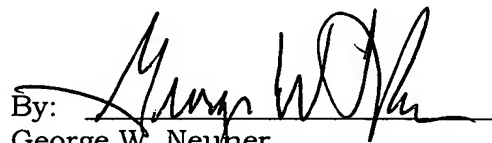
Applicants hereby elect the invention of Group II, claims 6-9 for further examination in the present application.

Applicants reserve the right to pursue the invention of Group I in a divisional application.

If for any reason a fee is required, a fee paid is inadequate or credit is owed for any excess fee paid, the Commissioner is hereby authorized and requested to charge Deposit Account No. **04-1105**.

Respectfully submitted,

Date: April 26, 2005

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